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SERIAL NUMBER 10/710,521	FILING DATE 07/18/2004 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. NAUP0591USA
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APPLICANTS

Yun-Ren Wang, Tai-Nan City, TAIWAN;

Ying-Wei Yen, Miao-Li Hsien, TAIWAN;
Shu-Yen Chan, Hsin-Chu Hsien, TAIWAN;

** CONTINUING DATA *****

None WLL

** FOREIGN APPLICATIONS *****

None WLL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 08/18/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	Examiner's Signature _____ Initials _____	TAIWAN	5	20	2

ADDRESS

027765
 NORTH AMERICA INTERNATIONAL PATENT OFFICE (NAIPC)
 P.O. BOX 506
 MERRIFIELD , VA
 22116

TITLE

METHOD FOR FABRICATING A SEMICONDUCTOR TRANSISTOR DEVICE HAVING ULTRA-SHALLOW SOURCE/DRAIN EXTENSIONS

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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